# SDD Review Document

## Initial Review

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| --- | --- |
| **Date** | 30-01-2021 |
| **Effort** | 2 hours |
| **Room/Location** | Google meet |
| **Review Status** | Closed |
| **Review name** | SDD\_DCM\_App.doc |
| **Method** | WT |
| **Release** | R3 |
| **Responsible** | Full Team (Edgar, Yael and Javier). |
| **Project** | Door Control Module |
| **Reason of Review** | Initial Release of Software Detail Design Document for DCM\_App SWC. |

## Second Review

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| --- | --- |
| **Date** | 01-02-2021 |
| **Effort** | 2 hours |
| **Room/Location** | Google meet |
| **Review Status** | Closed |
| **Review name** | SDD\_DCM\_App.doc |
| **Method** | WT |
| **Release** | R3 |
| **Responsible** | Full Team (Edgar, Yael and Javier). |
| **Project** | Door Control Module |
| **Reason of Review** | Review to solve the open points showed in the first review. |

## Comment List

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| --- | --- | --- | --- | --- | --- |
| **No.** | **Reference** | **Comments / Actions** | **Classification (E)rror/Risk / (R)emark** | **Responsible person/Planned date for completion** | **Completion(Name/Date)** |
| 1 |  | Fix Use Case diagram | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 31-01-2021 |
| 2 |  | Improve Line position on SWC Decomposition | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 31-01-2021 |
| 3 |  | Add initialize CAN callback to the DCM\_App\_Init | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 31-01-2021 |
| 4 |  | Consider extra button inputs for Manual Control activity (driver button), also add comment on the missing information. | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 01-02-2021 |
| 5 |  | Add callback to trigger Test\_CMD function | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 31-01-2021 |
| 6 |  | Update all activities for Cancel\_Window. | Error | Shiomar Salazar / 01/02/2021 | Shiomar S / 01-02-2021 |

## Check List

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| No | Description | OK / NOK / NR | Comment | Responsible person /  Planned date for completion | Status |
| 1 | Does the design comply to the SW architecture? (interfaces, scheduling...) | OK |  |  | Closed |
| 2 | Are all requirements allocated to Desing elements? | OK |  |  | Closed |
| 3 | Are all operations described in an adequate detail and with the adequate notation? | OK |  |  | Closed |
| 4 | Is the coupling level between SW parts (internal or externals) reduced to the minimum?  Is the justification of all global data written in the design document? | OK |  |  | Closed |
| 5 | Is each data owned by one unit?  If a data is public (for read and/or for write operations), is its access made using a method provided by the owner?  (if a method is provided for read and write operations on the same pubilc data, the data has to be private) | OK |  |  | Closed |
| 6 | How are the variables initialized? If not initialized, is the reason explained? | OK |  |  | Closed |
| 7 | Is the mechanism to initialise the functionality (when needed) described?  (eg: function calls, data acquisition …) | OK |  |  | Closed |
| 8 | In case of global variable (shared or not shared) used in reentrance function (reentrance raised by an ISR), is there a mechanism to avoid data modification during its treatment? | NR |  |  | Closed |
| 9 | Are Tasks, ISRs and event notification function kept as short as possible? | NR |  |  | Closed |
| 10 | Is the state variable only used in one single module?  (If the state variable needs to be visible from another module (to be avoided), indicate it in the design and use the mechanism of read copy on that variable). | NR |  |  | Closed |
| 11 | Is the event memorization (ex: flag) consumed at the end of each reccurence of a state machine?  Otherwise, the risk is to use an obsolete event (ex: event memorization consumption conditionned by a state transition). | NR |  |  | Closed |
| 12 | In case of asynchronous reception of the same event by several objects (ex: state machine, C function called periodicly…), has each object its own memorization mechanism (ex: separate flags). | NR |  |  | Closed |